

CURRICULUM VITAE

Name:- Miss. Ashwini K. Khairnar

Mob no: - 7263062657

Email ID - ashwinisk771@gmail.com

➤ **Career Objective:**

“To work in such an environment that utilizes my technical skills in the growth of the organization showing my abilities as an individual as well as a team player & also develops my personal skill.

➤ **Academic Qualification:**

Qualification	University / Board	Year of Passing	Percentage Aggrt.
M.E (Electrical Power System)	North Maharashtra University	Oct. - 2017	68.45 %
B.E (Electrical Engineering)	S.G.B. Amravati University	May-2014	77.25%
Diploma (Electrical Engineering)	Govt. Poly. Jalgaon	June -2011	76.50%
SSC	Nasik University	March 2008	85.84%

➤ **Computer Skills :**

- All India- Certificate in Information Technology (AI-CIT)
- AUTO-CAD 2010
- PS-CAD
- MATLAB

➤ **International Conference :**

1. Ashwini K. Khairnar, Dr. P. J. Shah, “Study of Various Types of Faults in HVDC Transmission System”, **International Conference on Global Trends in Signal Processing, Information Computing and Communication (ICSPICC - 2016)**, Technically Co- sponsored By IEEE Bombay Section ,SSBT’s College of Engineering and Technology, Maharashtra , India, 22-24 December 2016
2. Harish A. Patil, Ashwini K. Khairnar, Bhushan H. Patil, “Voltage Source Converter Based HVDC Transmission System”, **7th International Conference on Science, Technology & Management (ICSTM-17)**, (ISBN: 978-93-86171-30-6), Guru Gobind Singh Foundation’s Guru Gobind Singh Polytechnic, Nashik, February 2017.
3. Ashwini K. Khairnar, Harish A. Patil, Bhushan H. Patil, “ A Review Paper on VSC Based HVDC Transmission System”, **National Conference on Current Trends in Engineering, Science, Technology & Management [NACCTESTM-2017]** , pp. 453-457, March 2017
4. Ashwini K. Khairnar, Dr. P. J. Shah, “DC Line-To- Line Fault Analysis For VSC Based HVDC Transmission System”, **4th International Conference on “Latest Innovations in Science, Engineering and Management” (ICLISEM - 2017)**, (ISBN: 978-93-86171-51-1), Goa , pp.123-130, 28 June 2017

➤ **International Paper Published :**

1. Ashwini K. Khairnar, Dr. P. J. Shah, “Study of Various Types of Faults in HVDC Transmission System”, **IEEE International Conference on Global Trends in Signal Processing, Information Computing and Communication (ICSPICC - 2016)**, pp. 480-484, 2016.
2. Ashwini K. Khairnar, Dr. P. J. Shah, “DC Line-To-Ground Fault Analysis For VSC Based HVDC Transmission System”, **International Journal on Recent and Innovation Trends in Computing and Communication (IJRITCC)**, Volume No. 5 Issue 06, pp. 773-778, June 2017.

3. Bhushan H. Patil, Harish A. Patil, Ashwini K. Khairnar, “Voltage Source Converter Based HVDC Transmission System”, **International Journal of Electrical and Electronics Engineers** (ISSN 2321–2055), Volume No.09, Issue 01, pp. 551-558, January – June2017.
4. Ashwini K. Khairnar, Dr. P. J. Shah, “DC Line-To- Line Fault Analysis For VSC Based HVDC Transmission System”, **International Journal of Advance Research in Science and Engineering (IJARSE)**, Volume no. 6 , Issue No. 07, pp.29-36, July 2017.

➤ **EXPERIENCE :**

1. **SSBT’s College of Engineering & Technology, Bambhori , Jalgaon.**
 - Designation: **Assistant Professor** in Electrical Engineering Department
 - Period: From 17 July 2017 to Till Date
2. **College of Engineering & Technology North Maharashtra knowledge City”, Jalgaon , Maharashtra**
 - Designation: **Assistant Professor** in Electrical Engineering Department (Polytechnic)
 - Period: From 16 December 2015 to 03 April 2017
3. **College of Engineering & Technology North Maharashtra knowledge City”, Jalgaon , Maharashtra**
 - Designation: **Assistant Professor** in Electrical Engineering Department
 - Period: From date 01/09/2014 to Date 15/12/2015

➤ **PERSONAL DETAILS:**

- Name: Miss. Ashwini K. Khairnar
- Address: 506/6, Ishwar Colony, Near Datta Temple, Jalgaon,
- DOB: 7th Dec 1992
- Gender: Female
- Marital Status: Single
- Nationality: Indian
- Hobbies: Travelling , Reading , Listening song

➤ **DECLARATION:**

I consider myself familiar with Electrical Engineering aspects. I am also confident to work in a team. I hereby declare that the information furnished above is true to the best of my knowledge.

Date: / /2018

Place: Jalgaon

Signature

(Miss. Ashwini K. Khairnar)