

Curriculum Vitae



Personal Information:

Name: Vijay M. Deshmukh	Post: Associate Professor (E&TC)
Date of Birth: 17/06/1965	Category: open
Areas of specialization: Digital Signal Processing, Artificial Intelligence, Power Electronics, Photovoltaic Systems, Basic Electronics and Electrical, Feedback Control System, Electromagnetic Engineering, Network Analysis.	Address: SOJAR, Plot No.23,Gat No.65 +66,Vidyut Colony Jalgaon- M.S.- 425001 Mobile No.9890456078

1. Academic Information:

Academic record starting with graduation:

Examination	College / University / Institute	Year of Graduation	% or CGPA	Class / Division
B.E (Electronics)	Shri Sant Gajanan Maharaj College of Engg.Shegaon/Amravati University, Amravati.	1990	59%	Second
M.E.(Electrical)	Walchand College of Engineering Sangli./Shivaji University,Kolhapur.	1997	67%	First
Ph.D. “Comparison and Performance Analysis of PWM Inverter for Different KVA Ratings using a DSP”	North Maharashtra University, Jalgaon	Dec. 2017	---	----

2. Professional Information:

Teaching: 27 year Industrial: 00 years R&D Lab: 06 years Post PhD: 1 year

Employer	Position Held	From	To
SSBT College of Engineering and Technology and New Polytechnic Bambhori,Jalgaon	Lecturer	31/07/1990	31/07/1998
SSBT College of Engineering and Technology, Bambhori, Jalgaon	Lecturer	01/08/1998	13/02/2002
	Assistant Professor	13/02/2002	01/07/2010
	Associate Professor	01/07/2010	Till Date

2. Research Publications:

Item	Number
Journal Papers (SCI)	02
Journal Papers (other reputed journals) with impact factor	06
Books/conference proceedings	10
Monographs/ book chapters	00
Conference papers	14

(a) Journal Papers (SCI):

S. No.	Author(s). Title, Publisher, Volume no, Year, Page, etc., impact factor, website, through SCOPUS, Web of Science, Google Scholar, etc., (State applicable engine)
1	V.M. Deshmukh, A.J.Patil, “Comparison and performance analysis of closed loop controlled nonlinear system connected PWM inverter based on hybrid technique” Journal of Electrical Systems and Information Technology (JESIT), ELSEVIER . Volume 2(2015) PP.86-98.
2.	V.M. Deshmukh, A.J.Patil, P.V. Thakre, ” Development of MATLAB/SIMULINK model for three phase PWM inverter and hardware implementation and testing using DSP with nonlinear load” International Journal of Control Science and Engineering, Scientific & Academic Publishing (SAP) USA , Volume 5(2015) PP1-9.

(b) Other Published Journal papers not included in (a) above:

S. No.	Author(s). Title, Publisher, Volume no, Year, Page, etc., also include information on Impact factor, citation index, Year of starting of journal, frequency, website, ISSN no, indexed list in SCOPUS, Google Scholar, etc., (State applicable engine)
1	<p>V.M. Deshmukh, A.J.Patil, “Implementation of closed loop technique for improving the performances of PWM inverter- A review” International Journal of Engineering Research and Application (IJERA) Vol.4,Issue 10 (Part-4),October 2014,pp.73-78 ISSN:2248-9622. [DOAJ, Google Scholar, Ulrich Periodicals Directory, Crossref, ProQuest, Electronic Journals Library, Index Copernicus, NCBI,New Jour,europeana,BASE E-Z3]</p>
2	<p>Prashant.V.Thakre, V.M.Deshmkh, Dr.Saroj.Rangnekar, “Performance Analysis of Photovoltaic PWM Inverter with Converter for different carrier frequencies Using MATLAB” International Journal of Engineering and Technology, Singapore, IJET 2012 Vol.4 (3): ISSN: 1793-8244, pages 234-238. (IF – 1.42) [DOAJ, Engineering & Technology Digital Library, Google Scholar, Ulrich Periodicals Directory, Crossref, ProQuest, Electronic Journals Library, Index Copernicus, Chemical Abstracts Services (CAS)]</p>
3	<p>D. Khainar, V.M. Deshmukh “Performance Analysis of Diode Clamped 3 Level MOSFET Based Inverter”PP.1484-1489 International Electrical Engineering Journal (IEEJ), Vol. 5 (2014),ISSN NO.2027-2365 (0.261)</p>
4	<p>D. Khainar, V.M. Deshmukh “Review Study on Topologies of Multilevel Inverters”PP.19-24, International Journal of Innovative Research & Development (IJIRD), May 2014 Vol.3 ISSN NO.2278-0211(0.765)</p>
5	<p>Preeti V Murkute, V.M. Deshmukh Implementing the VOIP Communication Principles using Raspberry Pi as Server –pp 34-38,International Journal of Computer Application Volume 124- no.4 August 2015, ISSN NO0975-8887 (0.752).</p>
6	<p>Preeti V Murkute, V.M. Deshmukh A Review of Implementation of of VOIP COMMUNICATION on Embedded system pp-46-49,International Journal of Computer and Electronics Reaearch Volume 4 Issue 2, April 2025,ISSN NO.2320-9348(1.757)</p>

(c) Conference papers:

No.	Author(s). Title, Publisher, Volume no, Year, Page,details of conferences, Organizers, frequency, website, Sitation Index through SCOPUS, Google Scholar, etc., (State applicable engine)
1	Prashant Thakre, V.M.Deshmikh, Dr Saroj Rangnekar, "Performance Analysis of Bidirectional High Frequency Link Inverter for different carrier frequencies using MATLAB/Simulink ,Conference proceeding, February 19-20, 2011, ICPDRE2011 ,Chennai, page 18-21.
2	Dr. Prashant V. Thakre, Mr. V M Deshmukh, "Modeling, Simulation and Experimental Investigation of Closed Loop MPPT based Single Phase Stand Alone Photovoltaic System using Particle Swarm Optimisation Technique" CISCON 2017, Manipal University, 3 rd -4 th November 2017.

(d) Research Presentation/Publications of Faculty in Conferences/Seminar

Place	Conference/Seminar Title	Title of Paper	Dates	Academic year
Manipal University	CISCON 2017	Modeling, Simulation and Experimental Investigation of Closed Loop MPPT based Single Phase Stand Alone Photovoltaic System using Particle Swarm Optimisation Technique	3 rd -4 th November 2017	2017-18
Singapore	<i>IACSIT International Journal of Engineering and Technology, Vol. 4, No. 3, June 2012</i>	Performance Analysis of Photovoltaic PWM Single phase Inverter with Boost Converter for different carrier frequencies Using MATLAB	JUNE 2012	2012-2013
Chennai	International	Performance analysis of pv pwm inverter with Boost converter for different carrier frequency using matlab	19,20-02-2011	2011-2012
Chikhli	International	Fault Diagnosis Of Continuous Time System Using Parameter Estimation Technique	21,22-02-2008	2008-2009

Place	Conference/Seminar Title	Title of Paper	Dates	Academic year
Mumbai	International	Low Power VLSI Design Using Dynamic-Threshold Logic	4,5-02-2008	2008-2009-
Punjab	National	Hardware Implementation For Fast Convolution With A PN Code Using .FPGA	6,7-03-2007	2007-2008
Mumbai	National	Brain-Computer Interfaces	1-3-2006	2006-2007
Mumbai	National	Bio-Medical Signal Transmission Through Mobile Communication	1-3-2006	2006-2007
Mumbai	National	Active Character Technology Using Neural Networks	1-3-2006	2006-2007
Faizpur	National	Overview And Comparison Of STBC AND STTC	20,21-11-2006	2006-2007
Goa	National	Revolution in Information Transfer: Smart Phone	12-2-2006	2006-2007
Faizpur	National	An Adaptive Neuro Fuzzy System For Phace Based Optimal Image Thresholding	20,21-11-2006	2006-2007
Faizpur	National	Nanotechnology Vs Microtechnology	20,21-11-2006	2006-2007
Punjab	National	Remote Process Control By Using SMS	26-11-2005	2005-2006
Punjab	National	Simulation On Dead Time System With PID &Fuzzy Controller	26-11-2005	2005-2006
Faizpur	National	Internet Security	21-11-2005	2005-2006
Sangli	National	Vehicle Data Monitoring System	1,2-0-2005	2005-2006
Sangli	National	Library Automation System	1,2-0-2005	2005-2006

(e) Report of Training /Summer Schools, Winter Schools, seminar, Workshops, attended by Faculty

Place	Area	Month and Year	Academic year
SVNIT, Surat	<i>Power Conditioning and Distributed Power Generation</i>	26 th -30 th December 2016	2016-17
MANIT, Bhopal	<i>Digital Signal Processing & Processor</i>	31/12/12 to 4/1/2013	2012-2013
NMU, Jalgaon	<i>Research Methodology</i>	20/09/2012 to 24/09/2012	2012-2013
MANIT, Bhopal	<i>Advance Power Electronics & Real Time Digital Controllers</i>	4/6/2012 to 8/6/2012	2012-2013
MHRD, Remote SSBT	<i>Introduction to Research Methodology(Workshop)</i>	25-6-12 to 4-7-12	2012-2013
ICT(MHRD)Remote at KCE	<i>Solar photovoltaic fundamental technique and application. (Workshop)</i>	12-12-11 to 22-11-12	2011-2012
Jalgaon	<i>Syllabus framing for PG course in communication for NMU(Workshop)</i>	24-07-2010	2010-2011
Jalgaon	<i>Syllabus framing for PG course in Digital Electronics for NMU(Workshop)</i>	17-07-2010	2010-2011
Jalgaon	<i>Possibilities of Energy Saving in Industries & Energy Audit-2010(Workshop)</i>	17-03-2010	2009-2010
Faizpur	<i>Syllabus Framing For NMU (Workshop)</i>	17-08-2007	2007-2008
Dhule	<i>Syllabus Framing For NMU(Workshop)</i>	25-01-2003	2003-2004
Jalgaon	<i>Matlab And Simulink (Workshop)</i>	22,23-07-2003	2003-2004
Jalgaon	<i>Orientation Program For Engineering College Teachers (Workshop)</i>	11,13 -07-2003	2003-2004
Ahmedabad	<i>Biomedical Engineering (STTP)</i>	8,20- 05- 2000	2000-2001
Jalgaon	<i>Quality Engineering Education.</i>	26-12-1998	1997-1998

3. Research Guidance:

Category	Number	Name	Remarks
M.E. supervised	03	Mr. D.D.Khairnar Ms. Vandana Jadhav Ms. V.Murkute	-----
M.E. in progress	01	-----	On Going

4. OUTREACH Activities (Academic outreach activities (STTPs/workshop/conferences/organized etc.) with and outside College as Advisor/Convenor/Co-Convenor/Organizing Secretary/Expert Lecture delivered, etc.

Sr	Outreach Activities	Function Details
1	Nominated as members on Board of Studies (North Maharashtra University)	Member
2	LIC Committee(North Maharashtra University)	Member
3	Appointed as a Staff Selection Committee Member (North Maharashtra University)	Interview Panel Expert
4	Appointed as Recognize Post Graduate Teacher (North Maharashtra University)	PG Activity
5	Subject panel at NMU	Chairman
6	Avishkar 2016 (North Maharashtra University)	College level Coordinator
8	YUVARANG-2015(North Maharashtra University)	Member
9	Facilitation Centre Committee	Coordinator
10	IGNITEMIND	Judge
11	2K14	Judge
12	Sport Committee	Member
13	OPEN-HOUS	Member

14	Workshops Attended (17 nos.)	Participant
15	NAAC	Departmental Committee Member

5. ADMINISTRATION/Administrative Responsibilities (as per office order):
Dean/HOD/Prof In-charge/ Functional head

Sr	Category /Position	Duration	Function/details
1	Member, Sports of committee	August2012 onwards	Arranging annual sports.

Membership of Professional committees and activities, etc.,

Sr	Society	Function / Details
1.	I.S.T.E.- New Delhi	No.Lm25095 (Lifetime)
2	THE INSTITUTION OF ENGINEERS (India)	No.M/125744/1

6. Research Guidance

Master's Research guidance (Research / Academic problems) / Industrial live problem)

Sr	Type M.Tech. / M.Sc. etc.	Name	Completed / ongoing	Other guides, if any	Thesis Topic
1	M.E	Dnyaneshwar Khairnar	Completed	-----	Performance Analysis of Multilevel Inverter using Digital Controller for Photovoltaic grid Generator
2	M.E	Vandana Jadhav	Completed	-----	Modeling and Analysis of PF and THD for Power Electronic Converter
3	M.E	Preeti Murkute	Completed	-----	Implementation of Voice Over Internet Protocol (VoIP) Communication on Embedded System

Course material prepared, distributed to students and placed on web

Subject	Remarks
a) Feedback Control Systems	Course material and Lab manual
b) Electromagnetic Engineering	Course material and Lab manual
c) Networks Analysis and Synthesis	Course material and Lab manual

Developing / Developed New Courses

Sr	Details of new courses / Lab experiments
1.	Elements of Electrical and Electronics Engineering
2.	Signals and Systems

Dr. Vijay M. Deshmukh