



## SSBT's College of Engineering and Technology, Bambhori Jalgaon (Included under section 2 (f) and 12(B) of the UGC Act, 1956)

# potential

## Department of Electronics & Telecommunication Engineering

Volume XIII, Issue I

Department Newsletter : January – June 2022

### SSBT's College of Engineering and Technology at a Glance

SSBT's College of Engineering & Technology is an Engineering College governed by Shram Sadhana Bombay Trust (SSBT). It is a college with long tradition of imparting excellence in education.

- It is included under section 2(f) and 12(B) of UGC act 1956.
- QMS of College confirms to ISO 9000:2008.
- Approved by All India Council for Technical Education (AICTE), New Delhi.
- Permanent affiliation to N.M.U., Jalgaon.
- NBA Accredited from last 10 years.
- A Grade by N.M.U., Jalgaon
- Awarded Best Engineering College of Maharashtra by Engineering Education Foundation, Pune.
- Engineering Education Excellence Award-2015 by Indo Global Chamber of Commerce Industry & Agriculture, Pune.

### About Department

In the establishment year of college 1983, the Electronics Engg. Branch was started & as per the need of time it was converted to Electronic and Telecommunication from academic year 2001. The Department has got the NBA Accreditation by National Board of Accreditation (NBA) Committee constituted under AICTE with effect from 19/07/2008 for 5 Years and re-Accredited for 3 Years from 01-07-2014. Similarly the Institute is Accredited by National Assessment and Accreditation Council (NAAC) with CGPA of 2.91 on a seven point scale at B++ Grade, valid for a period of Five Years from 16-09-2016 (Vide letter No: F.19.26/EC(SC-17)/DO/2016/132.1, dated 19-09-2016). During the last 36 years, the department developed the twelve well equipped and furnished labs along with Seminar room, Departmental library, separate Departmental Computer Lab with soft wares like MATLAB, XILINX, DSP, UL-TIBOARD, ORCAD etc, & reception of Eklavya Channel from IIT Powai for students.

The E & TC students association organizes Curricular, Co-curricular, Cultural & Social activities for the overall development of students. The experienced & Qualified faculty, audio-video aids for teaching - learning process, organized visits to the industries, guest lectures of eminent personalities, inclined trend of academic results, rank holders at University level, success of students in competitive examinations & placement of students in renowned industries are some of achievements of the department.

### Department Mission

To develop Electronics & Telecommunication Engineers with patriotism and excellence to meet out the irresistible standards par locally and globally.

### Department Vision

The light of progressive knowledge and the brilliance of Electronics & Telecommunication Engineering is chasing the path towards Excellence for achieving an irreplaceable height in the global fraternity.

### Objectives of the Institute

- 1) To impart innovative teaching & learning.
- 2) To provide quality education with futuristic trends in Engineering & Technology.
- 3) To develop the institute as a research centre for academic excellence.
- 4) To ensure continual improvement in quality management system.
- 5) To inculcate social values, patriotism and professional ethics among the students.

### Features

- 1) Experienced & Qualified faculty members
- 2) Twice NBA Accredited
- 3) 12 Labs equipped with all state of art equipments
- 4) Research Lab & 1 UG Computer lab with latest configuration PCs
- 5) MATLAB, Xilinx, CC Studio etc. Software
- 6) NPTEL lectures
- 7) IEEE Student Branch
- 8) IETE Student Forum

### Salient Features

- **350+ Placement** in the last five years of E&TC Dept students.
- State of Art **well Equipped Laboratories and Recognized Research Center**
- Dedicated **Highly Qualified and Experienced Faculty**
- Tradition of **Excellent Results** at University Level
- **Expert Lectures and Industry visits** under industry institute interaction.
- **Funding for innovative student projects.**
- Student Participation at **Robocon National Level Competition.**
- Separate departmental Library and computer center with wi-fi facility.
- Conducting **PMKVY courses** in the department.
- MoU with BSNL, Mass-Tech Pvt Ltd, Electrosoft System and Yippee Technology Pvt. Ltd .

*Programme Educational Objectives (PEOs)*

**PEO 1. Core Knowledge** - To Built a strong foundation of electronics & telecommunication engineering required to solve engineering challenges.

**PEO 2. Employment/ Continuing Education** - To develop an ability to apply the technical skills for meeting the industrial needs of electronics & telecommunication field as well as academics.

**PEO 3. Professional Competency** - To empower the persona of electronics & telecommunication engineering graduates filled with professional and ethical responsibilities.

*Program Outcomes (POs)*

- **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
- **Problem analysis:** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
- **Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
- **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
- **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
- **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
- **Environment and sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
- **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
- **Individual and team work:** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
- **Communication:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
- **Project management and finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
- **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

*Program Specific Outcomes (PSOs)*

- An ability to apply the fundamental concepts and knowledge of core Electronics and Telecommunication engineering subjects for analysis, design and development of various electronics and telecommunication systems.
- An ability to solve complex Electronics and Telecommunication engineering problems using various electronic and telecommunication tools/equipments to demonstrate practical knowledge. .
- Exhibit proficiency and knowledge of interdisciplinary environment in demonstrating the work efficiency for industry and society to achieve a successful career / entrepreneur.

Workshop on PCB Design & manufacturing conducted by Alumni Mr.Vinay Badgujar held on 05-03-2022 at I lam onwards



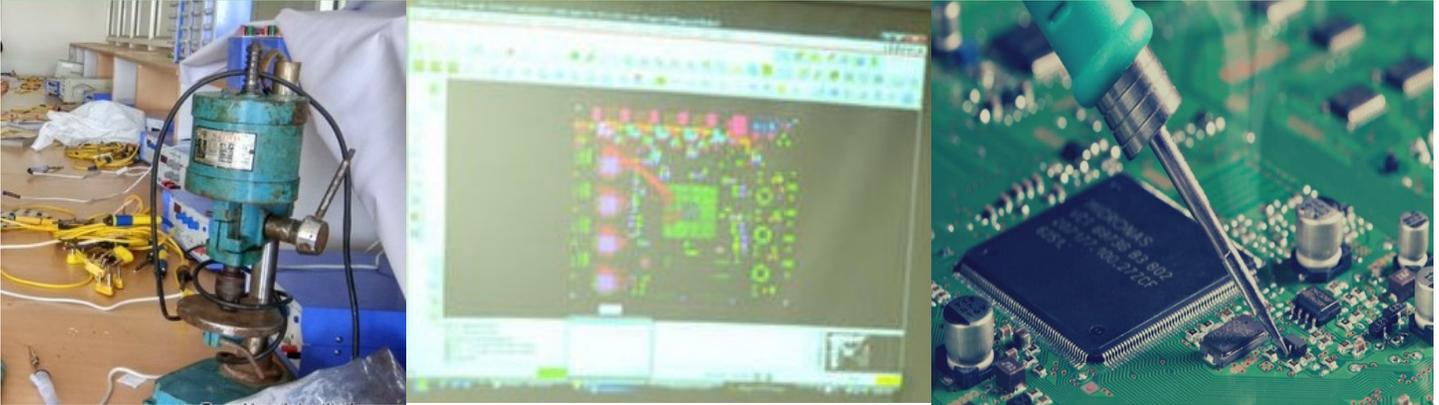
**Faculty Profile**

Sr No	Faculty Name	Designation	Qualification	Experience	Mobile No
1	<b>Dr. S. R. Suralkar</b>	Professor &Head	Ph.D	30 years	9421513244
2	<b>Dr. M. P. Deshmukh</b>	Professor	Ph.D	31 years	9422276792
3	<b>Dr. V. M. Deshmukh</b>	Associate Professor	Ph.D	30 years	9890456078
4	<b>Mr. N. M. Kazi</b>	Assistant Professor	M. E.(E.C.E.)	23 years	9422980311
5	<b>Mr. S. K. Khode</b>	Assistant Professor	M.E.(Digital Electronics)	14 years	8793347891

**Non Teaching Profile**

1	<b>Ms. Rajshri B. Patil</b>	Lab Technician	B.Sc.(Electronics)	24 years	9890637759
2	<b>Mr Vijay Patil</b>	Peon	H.S.C.	26 years	9421684217

Workshop on Workshop on PCB Design conducted by Alumni Mr.Vinay Badgujar held on 05-03-2022 at 2 pm onwards



Alumni Mr. Vinay Badgujar discussed on Printed circuit board (PCB) design brings your electronic circuits to life in the physical form. Using layout software, the PCB design process combines component placement and routing to define electrical connectivity on a manufactured circuit board. also he focused on hands on practice for PCB design. Finally Dr. S. R. Suralkar has given guidance on the IC fabrication technique and technologies , VLSI and CMOS technologies. Dr. Suralkar given appreciation letter to Mr vinay. All the student are satisfied and given good remark on conduction of the workshop.



www.cselectricalandelectronics.com

# Roadmap To Become PCB Design Engineer

Skills, Responsibility, Scope, Salary, Tools, Companies

	Decimal	DMS
Latitude	21.015016	21°0'54" N
Longitude	75.503029	75°30'10" E



	Decimal	DMS
Latitude	21.015028	21°0'54" N
Longitude	75.503033	75°30'10" E

	Decimal	DMS
Latitude	21.015016	21°0'54" N
Longitude	75.503029	75°30'10" E

### World Telecommunication Day Celebration on 17 May 2022



World Telecommunication Day has been celebrated annually on 17 May since 1969, marking the founding of ITU and the signing of the first International Telegraph Convention in 1865. It was instituted by the Plenipotentiary Conference in Malaga-Torremolinos in 1973 as Resolution 46.

### Traditional Day Celebration



Campus visit with our E&TC Alumni Mr. Nilesh Sable (2015 pass out)  
Testing Engineer at L&T Infotech Pune



Addon course conducted in the department for SE, TE and BE Students

SSBT's College of Engineering and Technology, Jalgaon  
Department of Electronics and Telecommunication Engineering  
Organizing  
On-line Add-on Course



on  
Emerging Trends in Electronics and Telecommunication Engineering  
between 03/03/2022 to 09/03/2022

**Importance of Addon Course**

The online add-on course gives you an insight about the "Emerging Trends in Electronics and Telecommunication Engineering" you want to pursue and also gives you a chance to change your path if your interest doesn't set in. Add on courses can be linked with Industry / current technology/ practical aspects of industry requirements. Such practical add on courses can help increasing employability of the students.

**Schedule of Addon Course**

Date	Time	Name of Faculty	Topic
03/03/2022	11:00 to 11:45	Dr. M. P. Deshmukh	Microelectronics circuit design - I
	12:00 to 12:45	Dr. M. P. Deshmukh	Microelectronics circuit design - II
	Recess		
	14:00 to 14:45	Dr. M. P. Deshmukh	Microelectronics circuit design - III
04/03/2022	11:00 to 11:45	Mr. N. M. Kazi	Basics of Audio Video Engineering - I
	12:00 to 12:45	Mr. N. M. Kazi	Basics of Audio Video Engineering - II
	Recess		
	14:00 to 14:45	Mr. N. M. Kazi	Use of AI & ML Electronics applications
05/03/2022	11:00 to 11:45	Dr. V. M. Deshmukh	Electric Vehicle & Smart Charging- I
	12:00 to 12:45	Dr. V. M. Deshmukh	Electric Vehicle & Smart Charging - II
	Recess		
	14:00 to 14:45	Dr. V. M. Deshmukh	Electric Vehicle & Smart Charging - III
07/03/2022	11:00 to 11:45	Dr. P. J. Shah	Advance VLSI Technology - I
	12:00 to 12:45	Dr. P. J. Shah	Advance VLSI Technology - II
	Recess		
	14:00 to 14:45	Dr. P. J. Shah	VLSI Design and applications
08/03/2022	11:00 to 11:45	Dr. S. R. Suralkar	Advance Trends in Embedded and RTOS
	12:00 to 12:45	Dr. S. R. Suralkar	Advance Trends in Embedded and RTOS
	Recess		
	14:00 to 14:45	Dr. S. R. Suralkar	Advance Trends in Embedded and RTOS

**Note:** Online Add-on course will be conducted on Zoom.



HOD E&TC



**Newsletter Editor:**  
**Mr. N. m. Kazi, Asst. Professor**

**Members:**  
BADGUJAR PREM EKNATH (TE E&TC)  
NAIR ANANDHU VINOD (TE E&TC)



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**&**  
**Dr. D. R. Shekhawat**  
**Chairman, GB**



**Shri. Raosaheb Shekhawat**  
**Managing Trustee**



**Dr. G. K. Patnaik**  
**Principal, SSBT's COET**



**Dr. S. R. Suralkar**  
**Professor & Head (E & TC)**